

GUIDE TO ARC FAULT DETECTION DEVICES (AFDDs)



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ABOUT BEAMA

BEAMA is the long established and respected trade association for the electrotechnical sector. The association has a strong track record in the development and implementation of standards to promote safety and product performance for the benefit of manufacturers and their customers.

This Guide provides guidance on Arc Fault Detection Devices (AFDDs) and their application in electrical installations.

This Guide has been produced by BEAMA's Building Electrical Systems Sector operating under the guidance and authority of BEAMA, supported by specialist central services for guidance on UK Internal Market, European Single Market, Quality Assurance, Legal and Health & Safety matters. BEAMA's Building Electrical Systems Sector comprises of major UK manufacturing companies.

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Acknowledgements

BEAMA would like to thank IEC, BSI and the IET for allowing references to their standards.

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1 INTRODUCTION

UK fire statistics identify that electrical fires are still unacceptably high. Use of overcurrent and residual current protection has vastly reduced the risk and consequence of electrical fires.

However, due to their characteristics, electric arcs in cables and connections cannot be detected by fuses, circuit breakers (e.g. MCBs, MCCBs) or by Residual Current Devices (RCDs), such electrical arcing can cause fires. Modern technology makes it possible to detect dangerous arcs and thus to protect installations. More specifically, an arc fault detection device (AFDDs) disconnects the circuit's electricity supply when it detects the presence of dangerous electrical arcs, thus preventing the outbreak of fire.

BS 7671:2018+A2:2022, the IET Wiring Regulations, mandate the use of AFDDs conforming to BS EN (IEC) 62606 as a means of providing additional protection against fire caused by arc faults in some AC final circuits. See Section 8 for further details.

This guide considers Arc Fault Detection Devices (AFDDs) according to BS EN (IEC) 62606 and their application within installations.



2 TERMINOLOGY AND DEFINITIONS

Arc	Luminous discharge of electricity across an insulating medium, usually accompanied by the partial volatilization of the electrodes.
Parallel arc fault	Arc fault where the arc current is flowing between active conductors in parallel with the load of the circuit.
Series arc fault	Arc fault where the current is flowing through the load(s) of the final circuit protected by an AFDD.
AFDD	<p>Arc Fault Detection Device – device intended to mitigate the effects of arcing faults by disconnecting the circuit when an arc fault is detected.</p> <p>NOTE: such devices include:</p> <ul style="list-style-type: none"> (a) a single device having opening means able to open the protected circuit in specified conditions, or (b) a single device with an integrated protective device, or (c) a separate unit assembled on site with a declared protective device. <p>The integrated protective device (b) or the declared protective device (c) for assembly on site is either a circuit-breaker in accordance with BS EN 60898-1 or an RCD in accordance with BS EN (IEC) 61008-1, BS EN (IEC) 61009-1 or BS EN (IEC) 62423.</p>
AFD unit	Part of the AFDD ensuring the function of detection and discrimination of dangerous earth, parallel and series arc faults and initiating the operation of the device to cause interruption of the current.
MCB	Miniature Circuit Breaker
MCCB	Moulded Case Circuit Breaker
RCD	Residual Current Device
RCCB	Residual Current Circuit Breaker without integral overcurrent protection
RCBO	Residual Current Circuit Breaker with integral overcurrent protection
Ring Final Circuit	A final circuit arranged in the form of a ring and connected to a single point of supply

3 OVERVIEW

Electrical fires continue to be a significant issue in UK installations. Electricity is a major cause of accidental fires in UK homes with over 17,000 electrical fires each year. Fire statistics¹ for 2017/18 identify that almost 23 % of domestic electrical fires are caused by faulty appliances and leads.

The 2017/18 fire statistics¹ also attribute 12 % of fires to electrical distribution (wiring cabling, plugs). These statistics demonstrate that electrical fires occur and can cause injuries, deaths and damage or destroy significant amounts of property. Electrical fires can be a silent killer occurring in areas of the home that are hidden from view and early detection. The objective is to protect such circuits in a manner that will reduce the risk of it being a source of an electrical fire.

The use of circuit breakers, fuses and RCDs greatly reduces the risk of fire. BS 7671:2008 Amd 3: 2015 introduced further requirements to minimise the spread of fire that may occur within a consumer unit.

ELECTRICAL FIRES CAN BE A SILENT KILLER OCCURRING IN AREAS OF THE HOME THAT ARE HIDDEN FROM VIEW AND EARLY DETECTION.

- Basic protection:** basic insulation of live parts or by barriers or enclosures
- Fault protection:** by protective earthing, protective equipotential bonding and automatic disconnection in case of a fault
- Additional protection:** for example, RCD not exceeding 30 mA

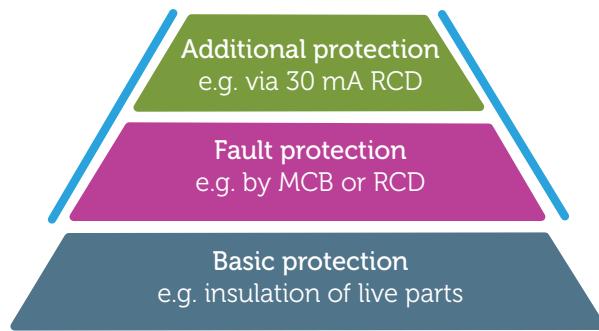


FIGURE 1 – EXISTING PROTECTION SCHEME

The above measures have made significant improvements in protection against the risk of fire. The use of AFDDs provide additional protection not offered by these measures as AFDDs are designed to detect low level hazardous arcing that circuit breakers, fuses and RCDs are not designed to detect. UK fire statistics for 2017/18 identify circa 12 % of electrical fires start within the electrical distribution system of an installation (wiring, cables, plugs), AFDDs detect series and parallel arcs which can occur within these cables and connections.

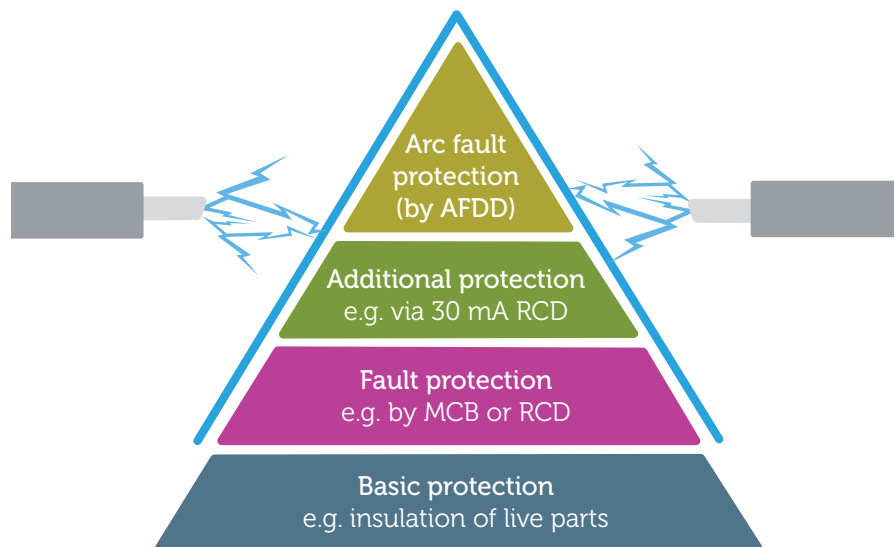
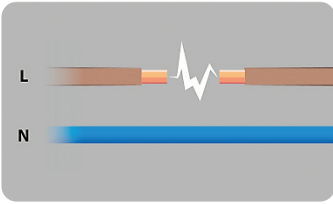


FIGURE 2 – ENHANCED PROTECTION, ADDING THE MISSING LINK

¹ Source: Department for Communities and Local Government, Fire Statistics 2017/18
² See BEAMA Technical Bulletin on Enhanced Fire Safety available on the BEAMA website

4

TYPES OF ARC FAULT



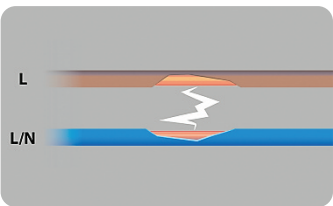
a) Series arc fault current

Originates from

- Damaged (e.g. crushed, broken, etc) cables
- Loose connections

A series arc is in series with a load and at a lower level than a parallel arc. The series arc fault characteristics result in the rms value of current and I^2t being too low to operate a fuse or MCB.

Protection is provided by AFDDs.



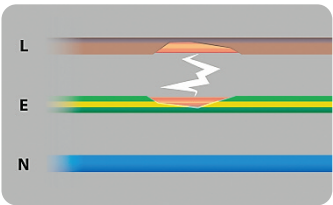
b) Parallel arc fault current (L-N)

Originates from :

- Fault between L-N
- High impedance due to damaged insulation, fault current is too low to trip other protection devices

Parallel arc fault characteristics (including short duration high peak currents) result in rms, I^2t , and peak time values that are generally too low to operate protective devices such as fuses or MCBs.

Protection is provided by AFDDs.

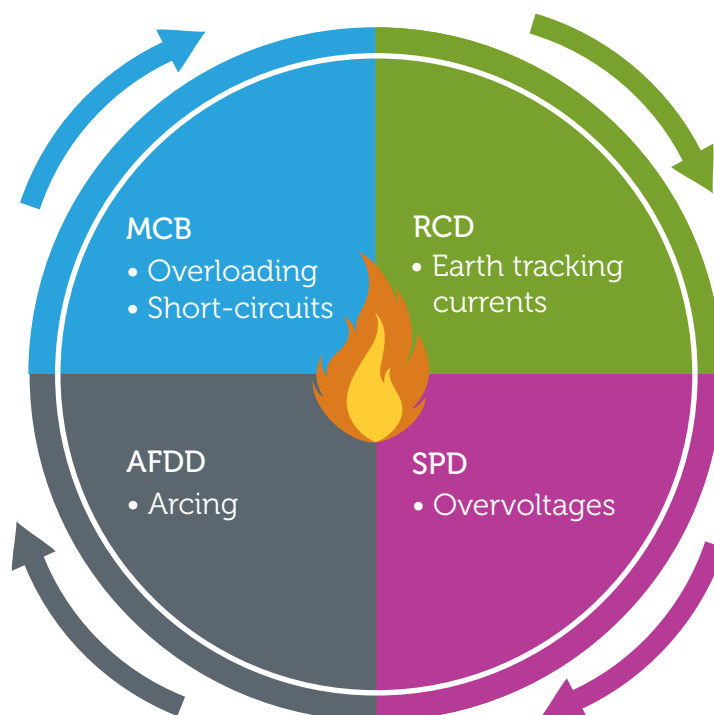


c) Parallel arc fault current (L-E)

Originates from :

- Fault between L-E
- High impedance due to damaged insulation, fault current is too low to operate circuit breakers or fuses

Protection is provided by RCDs and AFDDs.



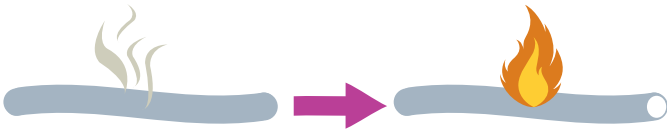
5 CAUSES OF SERIES AND PARALLEL ARC FAULTS

An arcing fault is an unintentional arcing condition in a circuit. Arcing creates high intensity heating at the point of the arc resulting in burning particles that may over time ignite surrounding material. Repeated arcing can create carbon paths that are the foundation for continued arcing, generating even higher temperatures. The temperatures of these arcs can exceed 6000 °C.

Development of an arc fault

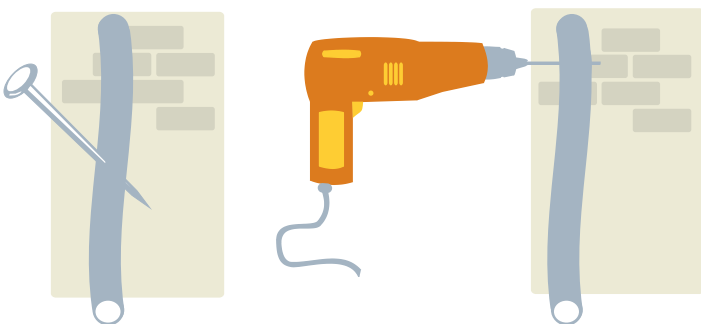
Arc faults are rarely instant and, depending on a wide number of factors, can take time to develop. The time for an arc fault to form is dependent on its root cause (external influences, ageing, etc.).

Arc faults can occur immediately or over a long period (hours, days, weeks, months, years). With the arc developing, temperatures up to 6000 °C can be generated and thus the surrounding insulation starts to burn and eventually a fire develops. The illustrations below illustrate a developing arc fault.

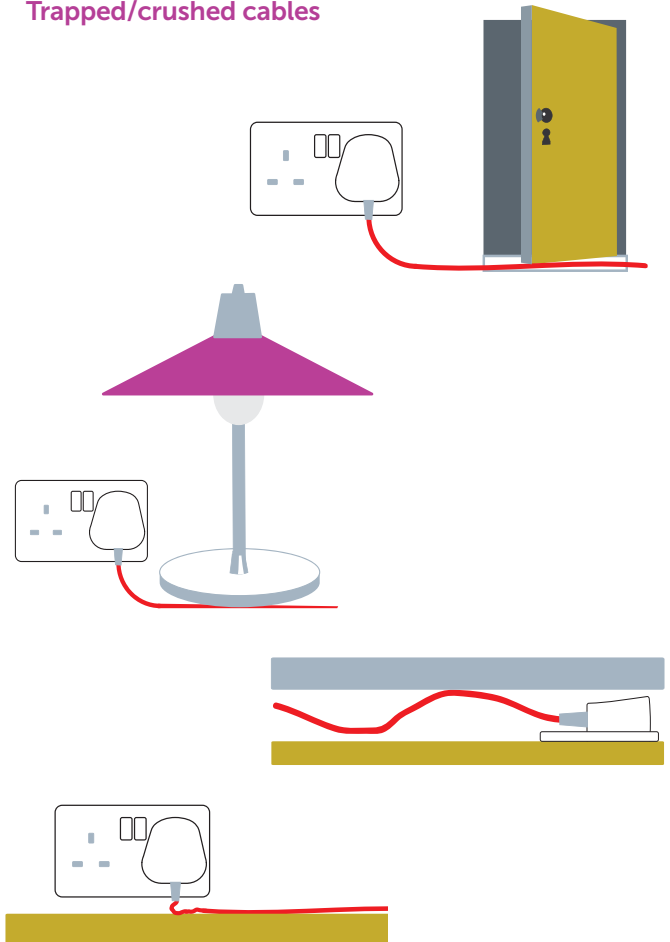


Arc faults can occur in many locations where electrical energy is present, with varied root causes, for example :

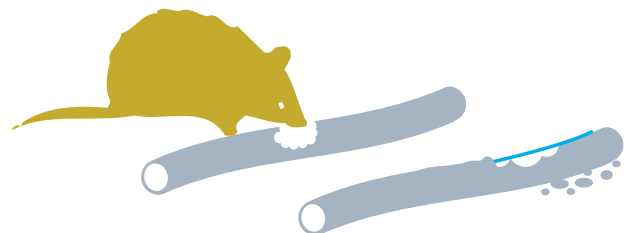
Pierced insulation



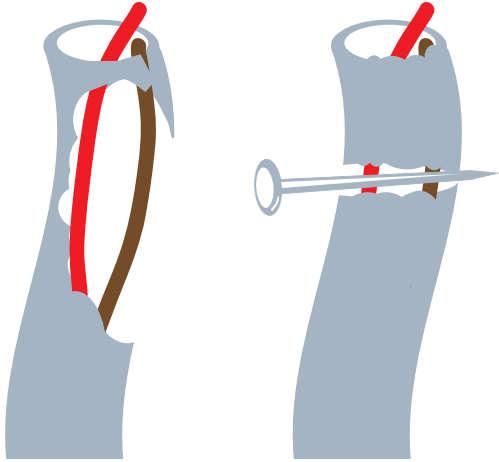
Trapped/crushed cables



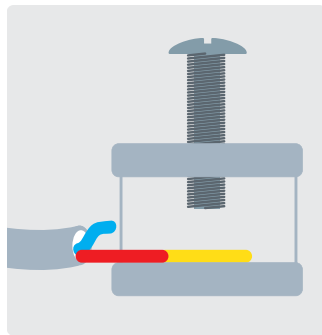
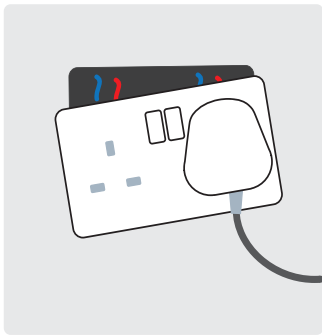
Rodent damage



Damaged insulation

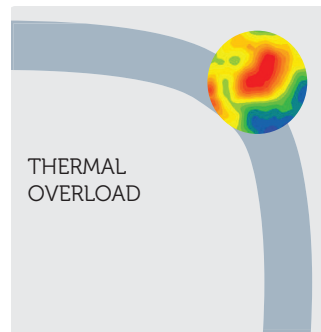
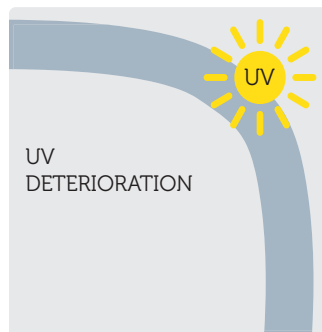
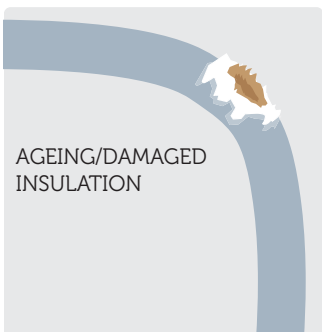


Loose terminations



NOTE: AFDDs will detect arcing but not high resistance connections within loose terminations. AFDDs will not detect high resistance connections due to trapped insulation.

Deteriorating insulation



6

HOW AFDDs WORK

Unlike a circuit breaker which detects overloads and short circuit currents and RCDs which detect current imbalance, an AFDD utilises electronic technology to analyse the signature (waveform) of an arc to differentiate between normal arcing and arcing faults. Although AFDD manufacturers may employ different technologies to analyse arcs, the end result is the same, detecting parallel arcs (line to line, line to neutral and line to earth) and series arcs (arcing within one of the conductors). Upon detection of an arcing fault, the AFDD disconnects the final circuit from the supply.

<https://vimeo.com/351569574>

<https://vimeo.com/351570442>

AFDD manufacturers test for numerous possible operating conditions and design their devices to constantly monitor for arcing faults.

In electrical circuits there are numerous cases of normal arcs appearing that correspond to typical operation, such as:

- Arcs created by switches, contactors, impulse switches, and other control devices when contacts are opened;
- Arcs created by motors of the different electrical loads connected to the circuit (portable electrical tools, vacuum cleaner motor, etc.)

To differentiate between normal arcing and arcing faults, the parameters analysed are both numerous and varied, such as:

- The signature (waveform) of the arc.
- Duration of the arc (very short durations, for example, are characteristic of the normal operation of a switch).
- Irregularity of the arc (the arcs of motors, for example, are fairly regular and as such are not considered an arc fault).

AFDDs are designed and tested to not respond to arcing under normal operation of equipment such as vacuum cleaners, drills, dimmers, switch mode power supplies, fluorescent lamps, etc. In addition, they are designed and tested to continue to respond to arc faults whilst the aforementioned equipment is being operated.

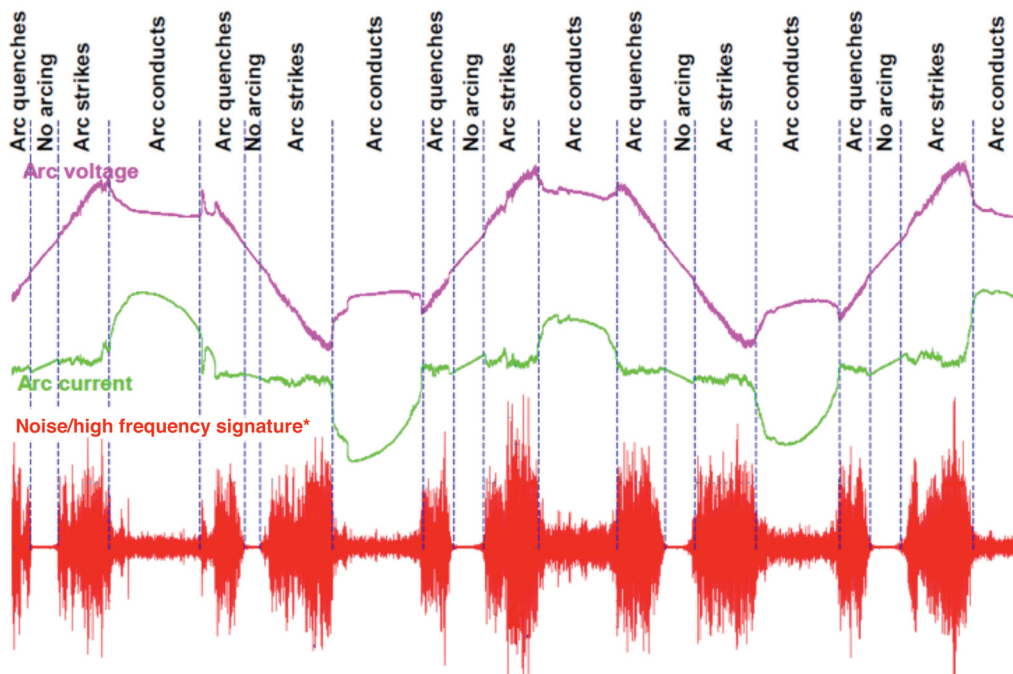


FIGURE 3 – TYPICAL SIGNATURE OF AN ELECTRIC ARC

*noise / high frequency signature exceeding 50 Hz which can be kHz or MHz and which correspond to an electric arc fault.

7 SELECTION AND INSTALLATION OF AFDDs

7.1. AFDDs are selected based on:

7.1.1. Method of construction

- AFDD as one single device, comprising an AFD unit and opening means and intended to be connected in series with a suitable short circuit protective device declared by the manufacturer complying with one or more of the following standards BS EN (IEC) 60898-1, BS EN (IEC) 61009-1 or BS EN (IEC) 60269 series.
- AFDD as one single device, comprising an AFD unit integrated in a protective device complying with one or more of the following standards BS EN (IEC) 60898-1, BS EN (IEC) 61008-1, BS EN (IEC) 61009-1 or BS (IEC) EN 62423.
- AFDD comprising of an AFD unit (add-on module) and a declared protective device, intended to be assembled on site.

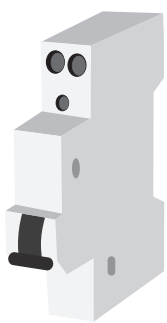


FIGURE 4
STANDALONE
AFDD (7.1.1 a)

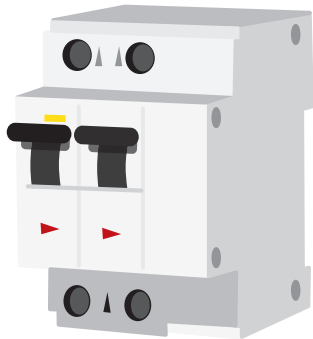


FIGURE 5A
AFDD INTEGRATED
WITH AN RCBO (7.1.1 b)

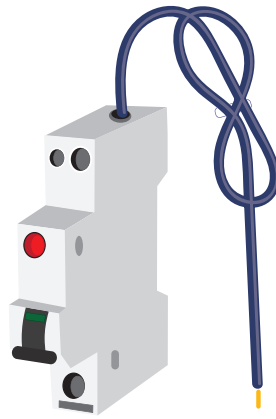


FIGURE 5B
AFDD INTEGRATED
WITH AN RCBO (7.1.1 b)

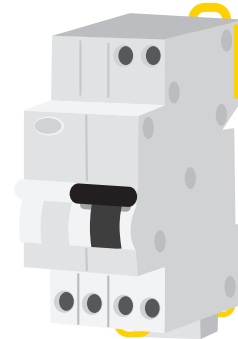


FIGURE 6
AFDD INTEGRATED
WITH AN MCB (7.1.1 b)

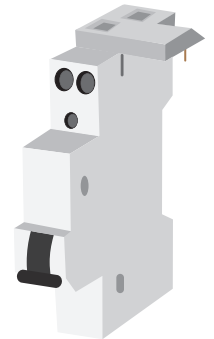


FIGURE 7
AFDD ADD-ON
MODULE (7.1.1 c)

7.1.2. Number of poles

- Two pole
- Three pole
- Four pole

7.1.3. Voltage rating

- 230 V
- 230/400 V
- 400 V

7.1.4. Current rating

Between 6 A and 63 A, preferred values are 6 – 8 – 10 – 13 – 16 – 20 – 25 – 32 – 40 – 50 – 63 A.

7.1.5. Characteristics

- Rated operational voltage (U_n)
The rated operational voltage of an AFDD is the value of voltage, assigned by the manufacturer, to which its performance is referred.
- Rated current (I_n)
The value of current, assigned to the AFDD by the manufacturer, which the AFDD can carry in uninterrupted duty.
- Rated conditional short-circuit current (I_{nc}) Value of the a.c. component of a prospective current, which an AFDD, protected by a suitable short-circuit protective

device in series can withstand under specified conditions of use and behaviour.

These three characteristics are marked on the AFDD.

7.2. Coordination

Where necessary, coordination of AFDDs with overcurrent protective devices is required.

7.2.1 Short-circuit coordination

BS EN (IEC) 62606 prescribes tests that are intended to verify that the AFDD, protected by the declared protective device, is able to withstand, without

damage, short-circuit currents up to its rated conditional short-circuit current (I_{nc}). An AFDD with an integrated overcurrent protective device (7.1.1 b) and 7.1.1 c)) provides the necessary coordination. An AFDD not having an integrated short-circuit protective device (7.1.1 a)) requires coordination in accordance with the manufacturer's instructions.

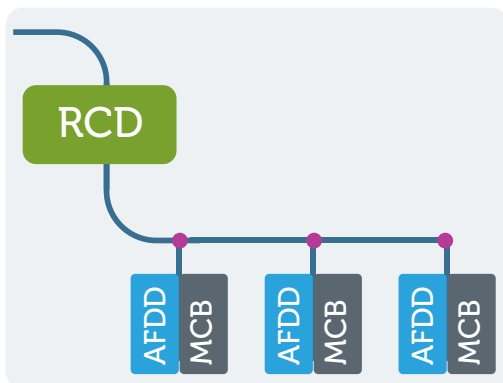
7.2.2 Selectivity coordination

7.2.2.1 Selectivity of an RCD supplying an AFDD integrated with an MCB

For a series arc fault or a parallel arc fault (L-N), the AFDD/MCB will operate without tripping the RCD thus selectivity is automatically achieved.

For a parallel arc fault (L-E), selectivity will be dependent on the characteristics and magnitude of the arc.

- Should the frequency and magnitude of the arc not correspond to the tripping characteristics of the RCD, then only the AFDD/MCB will trip.
- Should the frequency and magnitude of the arc correspond to the tripping characteristics of the RCD, then the RCD will trip and generally the AFDD/MCB will also trip.



7.2.2.2 Selectivity of an RCD supplying an AFDD integrated with an RCBO

For selectivity of an RCD with the AFDD element, 7.2.2.1 applies.

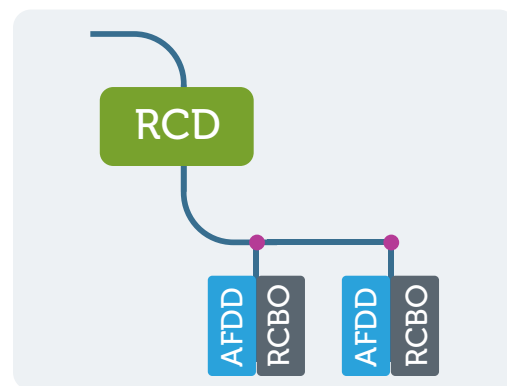
For selectivity of an RCD with the RCBO element, the following applies:

- the RCD is of selective type (type S or time delayed type with appropriate time delay setting), and
- the ratio of the rated residual current of the RCD to that of the RCBO is at least 3:1.

In case of RCDs with adjustable rated residual current and time delay, manufacturer instructions for selectivity should be followed.

NOTE 1: RCD type S is in accordance with BS EN (IEC) 61008 series or BS EN (IEC) 61009 series.

NOTE 2: A time-delay type RCD in accordance with BS EN (IEC) 60947- 2:2006, Annex B or Annex M will be marked with the symbol (Δt) followed by the limiting non-actuating time in ms or marked with an [S]



7.3. Installation of an AFDD

An AFDD shall be installed in accordance with the manufacturer's instructions.

7.4. Installation of an AFDD in assemblies

In low-voltage assemblies to the BS EN (IEC) 61439 series e.g. Consumer Units, Distribution Boards, incorporated devices, including AFDDs, shall only be those declared suitable according to the assembly manufacturer's instructions or literature.

7.5. AFDDs and parallel connected supplies

This includes generating systems such as wind, photovoltaic (PV) and battery storage systems. AFDDs that are marked line or load or by arrows indicating the direction of power flow, shall not be connected with the parallel supply on the LOAD side of the AFDD.

8

AFDDs AND THE WIRING REGULATIONS

The IET Wiring Regulations, BS 7671: 2018+A2:2022 Regulation 421.1.7 and 532.6 state the following:

421.1.7 Arc fault detection devices (AFDD) conforming to BS EN 62606 shall be provided for single-phase AC final circuits supplying socket-outlets with a rated current not exceeding 32 A in:

- Higher Risk Residential Buildings (HRRB)
- Houses in Multiple Occupation (HMO)
- Purpose built student accommodation
- Care homes

NOTE: Higher Risk Residential Buildings are assumed to be residential buildings over 18 m in height or in excess of six storeys, whichever is met first. It is anticipated that in many areas higher-risk residential buildings will be defined in legislation which can be subject to change over time, as well as in risk management procedures adopted by fire and rescue services. Current legislation should be applied.

For all other premises, the use of AFDDs conforming to BS EN 62606 is recommended³ for single-phase AC final circuits supplying socket-outlets not exceeding 32 A.

Where used, AFDDs shall be placed at the origin of the circuit to be protected.

The use of AFDDs does not obviate the need to apply one or more measures provided in other clauses in this standard.

NOTE: For busbar systems conforming to BS EN 61439-6 and Powertrack systems to BS EN 61534, the AFDD may be placed at a location other than the origin of the circuit.

532.6 Arc fault detection devices (AFDDs)

Where specified, arc fault detection devices shall be installed:

- (i) at the origin of the final circuits to be protected, and
- (ii) in AC single-phase circuits not exceeding 230 V.

AFDDs shall comply with BS EN 62606. Coordination of AFDDs with overcurrent protective devices, if necessary, shall take account of the manufacturer's instructions.

Furthermore, for medical locations, Regulation 710.421.1.201 states: In Medical locations of Group 1 and 2 Arc Fault Detection Devices (AFDDs) are not required to be installed. In medical locations of Group 0 Arc Fault Detection Devices (AFDDs) shall be used subject to a risk assessment.

³ To help users of the Wiring Regulations, BS 7671:2018+A2:2022 provides the following definition of recommendation and the verbal form which may be used to express it: Recommendation is the expression in the content of a document conveying that among several possibilities one is recommended as particularly suitable, without mentioning or excluding others. The verbal form which may be used to express recommendation is "should".

9

AFDDs AND RING FINAL CIRCUITS

In ring final circuits, an AFDD will afford the following protection:

1. Parallel arcing faults within the ring final circuit (see Note 1)
2. Parallel and series arcing faults in spurs off a ring final circuit
3. Parallel and series arcing faults in cables and equipment connected to the ring final circuit.

NOTE 1: In the event of a break in a leg of a ring final circuit, the load current will flow through the resulting radial circuits therefore, the risk of fire hazard due to series arcing is negligible.

10

TESTING AFDDs

AFDDs are provided with:

- a manual test button. When tested manually, the AFDD should trip. For AFDDs without an automatic test function, the test button should be pressed every six months
- an automatic test function that checks the arc detection circuit. The automatic test function consists of a test at switch-on and at intervals not exceeding at least once a day. During this automatic testing, the AFDD does not trip unless a malfunction is detected. In case a malfunction is detected the AFDD will trip and indicate a malfunction.

For AFDDs with both a test button and automatic test function, manufacturer's instructions shall be followed with respect to test button operation.

BS 7671:2018+A2:2022 appendix 6 refers.

AFDDs integrated with an RCCB or RCBO will at least include a test button for the RCD element of the device.

There is no requirement in BS EN (IEC) 62606 or in BS 7671 to test an AFDD in the electrical installation with an external test instrument.

BEAMA recommends disconnecting AFDDs during fixed installation insulation resistance testing at 500 V DC. If this is not practical, the DC voltage can be reduced to 250 V but specific manufacturer's guidance shall be followed.

11

FREQUENTLY ASKED QUESTIONS

Q.1 Will an AFDD trip on any kind of arcing?

No, the AFDD is designed to differentiate between what is known as dangerous arcing and arcing associated with normal operation of equipment.

Dangerous arcing is when a continuous (long duration) arc is established creating sufficient energy to cause ignition; e.g. ignition of cable insulation.

A long duration arc is one that exceeds the maximum break times (known as trip time) stated in BS EN 62606 (see Q2).

Arcing associated with normal operation e.g. switching, motor brushes, is short duration arcing – sometimes referred to as ‘sparking’.

Q.2 What is the trip time of an AFDD?

The trip time of an AFDD is dependent on the arcing current. For increasing levels of arc current the trip time decreases. BS EN 62606 Table 1 lists the following maximum break (trip) times :

Test arc current (r.m.s. values)	2,5 A	5 A	10 A	16 A	32 A	63 A
Maximum break time	1 s	0,5 s	0,25 s	0,15 s	0,12 s	0,12 s

Q.3 When required for additional fire protection, RCDs shall be a rated residual operating current not exceeding 300 mA. Why is the minimum AFDD tripping current of 2.5 A so much greater than 300 mA?

300 mA (0.3 A) equates to: $230 \text{ V} \times 0.3 \text{ A} = 69 \text{ W}$ which is related to leakage current and not arc current. RCDs do not detect the specific waveform / signature associated with a stable electric arc. The AFDD tripping time at 2.5 A relates to approximately 100 W and can be explained as follows: The break (tripping) time in BS EN 62606 for AFDDs, is derived from the energy to ignite a cable by degrading the insulation with contact arcing and glowing. The minimum energy value of 100 J with an arc voltage of 40 V was established for the tripping characteristic for series arcing. The total break time t_B is therefore derived as follows:

$$t_B = \frac{100 \text{ J}}{40 \text{ V } I_{arc}} = \frac{2.5 \text{ A s}}{I_{arc}} \text{ for } I_{arc} \leq 20 \text{ A and } t_B = 0.12 \text{ s for } I_{arc} > 20 \text{ A}$$

100 W for 1 s equates to 100 J, so the AFDD can promptly interrupt the current and limit the duration of combustion of the cable, thus significantly reducing the risk of the fire spreading. AFDDs detect the specific waveform / signature associated with a stable electric arc.

Q.4 Will an AFDD trip if I create an arc manually?

It is very difficult to manually create an arc of sufficient current magnitude and duration to trip an AFDD. See section 6 above.

Intermittent touching of conductors together will create numerous short duration arcs (sparks), these arcs (sparks) do not create sufficient arcing current and time duration to trip an AFDD.

Q.5 Under what conditions could a high resistance connection develop arcing and an AFDD detect the arc?

At an electrical connection which clamps on the conductor insulation or which is incorrectly torqued, excessive ohmic heating may occur without sustained arcing and therefore an AFDD would not operate. However, if the connection subsequently deteriorates as a result of the heating, sustained arcing can occur that will operate the AFDD.

A high resistance connection can undergo progressive deterioration e.g. high resistance creates localized heating, heating increases oxidation and creep, the connection becomes less tight, which can result in carbonization and sustained arc tracking which will operate the AFDD.

Q.6 Do AFDDs work on low load-current circuits?

For series arc faults below 2.5 A an AFDD according to BS EN 62606 is not required to trip. However, this does not negate the need for an AFDD as the risk of a parallel arc fault greater than 2.5 A is very probable, irrespective of the low current load.

Q.7 Why is a 2.5 A or higher arc fault considered dangerous when it is known that currents below 2.5 A can cause ignition?

2.5 A is the arc current and not the circuit current. An arc current below 2.5 A does not dissipate enough power to cause ignition. In a high resistance electrical connection, the circuit current below 2.5 A could cause ignition but this is not an arcing current.

Q.8 Why was 2.5 A selected as the lowest test arc current?

Based on the probability of cable insulation ignition, values of arc current less than 2.5 A presented a lower risk.

Q.9 Are AFDDs intended to protect more than one final circuit?

No. An AFDD should be placed at the origin of a single final circuit. This arrangement is prescribed in the AFDD product standard to minimise unwanted tripping.

Installation designs should meet the requirements of BS 7671. The fundamental principle in Regulation 314.1 is that every installation shall be divided into circuits, as necessary, to avoid danger and minimize inconvenience in the event of a fault and to reduce the possibility of unwanted tripping.

Q.10 Do AFDDs require SPD protection?

There is no particular requirement to use external SPDs with an AFDD. The AFDD will have internal protection enabling it to conform to the surge tests described in BS EN 62606.

The need for any transient overvoltage protection in the installation is specified in BS 7671.

Q.11 Are AFDDs suitable for EV charging circuits and equipment?

Yes, AFDDs are suitable for the protection of EV charging circuits and equipment.

Q.12 Does AFDD Regulation 421.1.7 apply to socket-outlets and connectors of Electric Vehicle Supply Equipment conforming to BS EN IEC 61851?

The electrical installation designer will need to meet the intent of Regulation 421.1.7. Where Electric Vehicle Supply Equipment incorporates connection products conforming to BS EN 62196, these might not be considered to be socket-outlets within the scope of Regulation 421.1.7.

Q.13 Are AFDDs available for three phase circuits?

Yes, BS EN 62606 covers 3-phase AFDDs however currently there is limited availability and will depend on the manufacturer.

Q.14 Will there be a need for a three-phase AFDD?

At this stage it is not possible to predict future requirements however, BS 7671:2018+A2:2022 only mandates/recommends that single phase AC final circuits supplying socket-outlets (up to 32A) be protected by AFDDs, depending on building type.

Q.15 Will the trip flashing codes be standardised across all brands?

BS EN 62606 does not specify indication colour or codes; standardisation may be considered as a proposal for further amendment of the standard.

Q.16 Will welding equipment cause unwanted tripping of an AFDD?

A welder separates the primary supply from the secondary supply which creates the arc for welding therefore, the welding arc is not detected by the AFDD supplying the welder.

An inverter operates by increasing the primary power supply frequency to the transformer, this creates a different frequency signature to that of an arc fault frequency signature therefore, the AFDD will not trip due to the normal load characteristics of the inverter.

Q.17 Are AFDDs parasitic loads? What are the electricity costs to operate an AFDDs?

Parasitic loads consume power when in stand-by mode, waiting for a command to perform their main function. AFDDs are not parasitic loads; similar to smoke alarms, they perform a safety function by constantly monitoring/protecting an installation.

The running cost will depend on the number of installed AFDDs and the energy tariff. In comparison to a number of electrical appliances that require charging or consume energy in standby mode, AFDDs' consumption of power to improve safety may not be considered significant. It should be noted that some electronic protection devices e.g. electronic RCBOs also consume power to provide constant monitoring/protection of an installation.

Q.18 Will AFDDs be subject to unwanted tripping if harmonics are present?

BS EN 62606 contains tests to verify that the AFDD shall not trip when subjected to various loads that could cause unwanted tripping, including harmonics.

Q.19 Do AFDDs dissipate heat similar to other protection devices, and do they require testing in consumer units and distribution boards?

AFDDs will dissipate some heat however assemblies with AFDDs installed should be temperature rise tested according to the relevant assembly standard e.g. consumer unit or distribution board. The assembly manufacturer's instructions are required to provide information on rated currents and any derating factors (RDF).

Q.20 Does the use of cables with XLPE insulation mitigate the need for AFDDs?

No. Although XLPE insulation doesn't char when exposed to arcing, numerous items of connected equipment will use polymers that do char and ignite when exposed to an arc fault. Also, if the insulation is damaged exposing live conductors, these can be bridged through contamination resulting in char / carbon deposits and an arc fault.

Q.21 How do you identify what type of fault has caused a tripping event?

Although not required by BS EN 62606, generally, visual indication is provided on the AFDD to help determine the type of fault, refer to manufacturer's instructions.

Q.22 How do you fault-find arc fault events?

Generally, visual indication is provided on the AFDD to determine the type of arc fault, refer to manufacturer's instructions.

Fault finding for arc faults would use the same methodology as other protection devices e.g. resistance, insulation measurements.

Q.23 For retrofit, can you replace an MCB or an RCBO with an AFDD?

Retrofit of AFDDs will depend on the assembly (e.g. consumer unit, distribution board) construction/configuration, assembly manufacturer's instructions must be followed.

Q.24 Can upstream faults trip an AFDD e.g. Local Area Network faults?

There is a very low likelihood of unwanted tripping due to upstream disturbances due to the arcing signal attenuation and circuit architecture.

Upstream parallel arc disturbances would not be detected by AFDDs. For an upstream series arc disturbance, the upstream signature is different (and attenuated) from a downstream series arc fault signature which minimises the possibility of unwanted tripping.

Q.25 Can you clarify if overload/overcurrent protection is provided by an AFDD?

AFDDs come in various configurations:

- Stand-alone AFDD – provides arc fault protection only.
- AFDD integrated in, or assembled with, an MCB – provides overcurrent and arc fault protection.
- AFDD integrated in, or assembled with, an RCCB – provides residual current and arc fault protection.
- AFDD integrated in, or assembled with, an RCBO – provides overcurrent, residual current and arc fault protection.

The appropriate AFDD configuration should be selected for the application.

Q.26 Does an AFDD provide better arc fault detection / protection for parallel Live to Earth arc faults than an RCD, particularly a 30 mA RCD?

Yes. An RCD will detect most parallel arc faults to earth however, the correct operation of the RCD is not 100 % ensured in all cases, since the RCD is only tested with a continuous test current of defined frequencies.

Q.27 Can the success of Arc Fault Circuit Interrupters (AFCIs) in the US, indicate how AFDDs could provide similar protection in the UK?

Yes, because:

- The UK has approximately two times higher line voltage and arc power than the US 120 V system. The arc fault power is not directly a function of the line voltage, it is the product of arc fault current and arc fault voltage.
- For a series arc fault, the AFCI product standard is 5 A and the AFDD standard is lower at 2.5 A,

Examples of AFCI protection success can be viewed at <https://www.afcisafety.org/files/NEMA-Success-Stories.pdf>

Q.28 Given the difference in building materials/methods between the US and UK, with the US building a lot of timber-based buildings, are AFDDs really beneficial in UK buildings?

Although the UK still uses brick and block building method, timber frame building has become much more widespread. Electrical fires due to arcing are a risk irrespective of building method, wherever combustible building materials exist (timber frame, roof space, internal stud walls), electrical fires due to arcing are a risk.

In addition, AFDDs also protect against electrical fires due to other arcing risks (e.g. damaged appliance leads, extension leads) in the vicinity of combustible materials within the property.

Q.29 Are AFDDs intended for new installations and additions which should be undertaken to acceptable quality levels and good workmanship?

Yes, however it should be noted that AFDDs are not intended to verify the quality of the installation work, they are not a substitute for good installation practice. They will however detect loose connections that result in arcing. AFDDs are intended to protect against arcing due to installation damage, carelessness by the user, misuse or abuse of the installation and/or the connected supply leads/appliances.



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